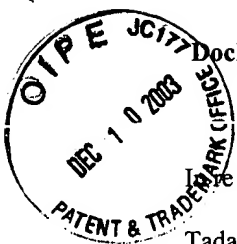


2188



Docket No.: 57454-066

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Tadaaki YAMAUCHI, et al.
 Serial No.: 09/823,996
 Filed: April 03, 2001
 Customer Number: 20277
 Confirmation Number: 6448
 Group Art Unit: 2188
 Examiner: Jasmine Song

For: SEMICONDUCTOR MEMORY DEVICE INTERNALLY PROVIDED WITH LOGIC CIRCUIT WHICH CAN BE READILY CONTROLLED AND CONTROLLING METHOD THEREOF

Mail Stop Non-Fee Amendment
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

RECEIVED
 DEC 11 2003
 Technology Center 2100

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☒ No additional fee is required.
- ☒ Applicant is entitled to small entity status under 37 CFR 1.27
- ☒ Also attached: Copy of IDS and mailing card filed on Apr. 3, 2001

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	16	20	0	\$18.00 =	\$0.00
Independent Claims	3	3	0	\$86.00 =	\$0.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Alexander V. Yampolsky
 Registration No. 36,324

600 13th Street, N.W.
 Washington, DC 20005-3096
 (202) 756-8000 AVY:LCF
 Facsimile: (202) 756-8087
 Date: December 10, 2003
 WDC99 851837-1.057454.0066

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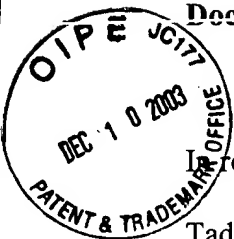
a

7/A
EWm
12-16-03

Docket No.: 57454-066

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of

Tadaaki YAMAUCHI, et al.

Serial No.: 09/823,996

Filed: April 03, 2001

For: SEMICONDUCTOR MEMORY DEVICE INTERNALLY PROVIDED WITH LOGIC
CIRCUIT WHICH CAN BE READILY CONTROLLED AND CONTROLLING
METHOD THEREOF

: Customer Number: 20277
:
: Confirmation Number: 6448
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: Group Art Unit: 2188
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: Examiner: Jasmine Song
:

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Technology Center 2100

AMENDMENT

Mail Stop Non-fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed September 10, 2003, please amend the above-
identified application as follows:

a